



Department of Electronics & Communication Engineering

राष्ट्रीय प्रौद्योगिकी संस्थान, मणिपुर

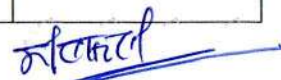
NATIONAL INSTITUTE OF TECHNOLOGY MANIPUR

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(An Autonomous Institute under MHRD, Govt. of India)

Subject allocation (Jan to May 2024)

Sl no	Faculty	Theory (3-0-0-6)	Practical(0-0-3-3)	Total <i>Credit</i>
1.	Dr A Dinamani Singh	Analog Communication(EC204)	Analog Communication Lab(EC234)	9
2.	Dr Ashish Ranjan	Low Power VLSI(VL504)		6
3.	Dr Manoj Kumar	VHDL(EC310)	VLSI & VHDL Lab(EC332)	9
4	Dr Kalyan Mondal	Antenna Design(EC308)	Antenna Design Lab(EC338)	9
5.	Dr L Surajkumar Singh	Electronics and Electrical Measurements and Instrumentation (EC206)	Electronics and Electrical Measurements and Instrumentation Lab (EC236)	9
6	Dr W Vandana	Basic Electronics Engineering (EC101)		12
		Semi conductor IC Technology(EC484)		
7	Dr H TarunKumar	Signals systems Analysis(EC208)		12
		VLSI Design(EC302)		
8	Dr S Jayananda Singh	Wireless and Mobile communication (EC 462)		12
		Advanced Microwave Engg(EC506)		
9	Dr Kh JilenKumari	Industrial Electronics(EC306)	Industrial Electronics Lab(EC336)	15
		Advanced Digital Communication (EC504)		
10	Dr Ng Chitralekha	Memory Technologies(VL532)	System Simulation Lab-A (VL514)	15
		Semi conductor IC Technology(VL484)		
11	Dr K Pritamdas	Image Processing Technique(EC594)	Image Processing Lab(EC516)	9
12	S Devayani Devi	Mobile Communication System(EC472)		12
		Data Communication(EC552)		
13	Th Churchill	Fiber Optics Communication(EC474)	Embedded System Lab(334)	15
		Embedded System(304)		
14	N Richard	Digital Electronics Circuit(EC202)	Digital Electronics Circuit Lab(EC232)	15
		Real Time Operating System(VL506)		


Prof. A Dinamani Singh
(HOD, ECE Dept)

Faculty	Technical Assistant
Prof A Dinamani Singh=ADS Dr Ashish Ranjan =AR Dr Manoj Kumar =MK Dr L SurajKumar Singh= LSS Dr Kalyan Mondal=KM Dr W Vandana Devi=WVD Dr H Tarun Kumar=HTK Dr S Jayananda=SJ Dr K Pritamdas =KP Dr Ng Chitraleka =CNG Dr KhJilenkumari=JK Th Churchil =TK S Devayani =SD Richard N=RN	Dr.Y Shantikumar=YSS Anupa H=AH S Nonglenkhomba =SN Robert O=RO N Debala=ND

4 th Sem, Subject	6 th Sem, Subject
Probability and Random Process=PRP Digital Electronics Circuits = DEC Analog Communication = AC Electronics and Electrical Measurement and Instrumentation = EEMI Signals and System Analysis =SSA	VLSI Design=VD Embedded System=ES Industrial Electronics=IE Antenna Design=AD VHDL
8 th Sem, Subject	M.Tech 2 nd (Communication and Signal Processing),Subject
Wireless and Mobile Communication=WMC Fiber Optics Communication=FOC Mobile Communication System=MCS Semiconductor IC Technology=SICT Professional Ethics=PE	Image Processing Technique=IP Advanced Digital Communication=ADC Advanced Microwave Engineering=AME Data Communication=DC Image Processing Lab=IP
M.Tech 2 nd (VLSI),Subject	
Semiconductor IC Technology=SICT(M) Low Power VLSI=LPV Real Time Operating System=RTOS Memory Technologies=MT System Simulation Lab=SSLab	

Time Table 2024(Even Semester)

B Tech Semester IV Electronics& Communication Engineering(Room:LH7)

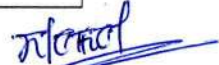
Day	9:00am -10:00am	10:00am -11:00am	11:00am -12:00am	12:00am - 2:00pm	2:00pm-3:00pm	3:00pm- 4:00pm	4:00pm -5:00pm
M	PRP(MA202)	DEC(EC206) /RN	AC(EC204)/ADS	LUNCH	DEC -LAB (EC232) (GROUP -I) /YSS,AH, Vivek Bhatt AC -LAB (EC234) (GROUP -II)/SN,RO, Md Azarruddin		
T	PRP(MA202)	SSA(EC206) /HTK	AC(EC204) /ADS		DEC -LAB (EC232) (GROUP -II) /YSS,AH, Vivek Bhatt AC -LAB (EC234) (GROUP -I) /SN,RO, Md Azarruddin		
W	PRP(MA202)	EEMI (EC206)/LSS	AC(EC204) /ADS		EEMI -LAB(GROUP -I)/AH,Bimoljit		
T	DEC(EC206)/RN	EEMI(EC206) /LSS	SSA(EC206) /HTK		EEMI -LAB(GROUP -II)/AH,Bimoljit		
F	EEMI(EC206) /LSS	DEC(EC206) /RN	SSA(EC206) /HTK		NSS/NCC		

B Tech Semester VI Electronics& Communication Engineering(Room: LH 8)

Day	9:00am -10:00am	10:00am -11:00am	11:00am -12:00am	12:00am - 2:00pm	2:00pm-3:00pm	3:00pm- 4:00pm	4:00pm -5:00pm
M	ES(EC304)/TS	AD(EC308)/KM	VLSI(EC302)/HTK	LUNCH	NSS/NCC		
T	IE(EC306)/JK	ES(EC304)/TS	VHDL(EC310)/MK		IE LAB(GROUP-I)(EC336)/ND,Linthoi AD LAB(GROUP-II)(EC338)/RO,Deepak		
W	AD(EC308)/KM	IE(EC306)/JK	VLSI(EC302)/HTK		IE LAB(GROUP-II) (EC336)/ND,Linthoi AD LAB(GROUP-I) (EC338)/RO,Deepak		
T	VHDL(EC310)/MK	AD(EC308)/KM	IE(EC306)/JK		VLSI & VHDL LAB(GROUP-I) (EC332)/YSS,Joshana ES LAB(GROUP-II) (EC334)/SN,Chinglemba		
F	VLSI(EC302)/HTK	VHDL(EC310)/MK	ES(EC304)/TS		VLSI & VHDL LAB(GROUP-II) (EC332) /YSS,Joshana ES LAB(GROUP-I) (EC334)/SN,Chinglemba		

BTech Semester VIII Electronics& Communication Engineering(Room: LH14)

Day	9:00am -10:00am	10:00am -11:00am	11:00am -12:00am	12:00am - 2:00pm	2:00pm-3:00pm	3:00pm- 4:00pm	4:00pm -5:00pm
M	PE(HS402)	WC(EC462)/SJ	FOC(EC474)/TS	LUNCH	PROJECT-II (EC432)		
T	PE(HS402)	SICT(EC484)/WVD	WC(EC462)/SJ		PROJECT-II (EC432)		
W	MCS(EC472)/SD	SICT(EC484)/WVD	FOC(EC474)/TS		PROJECT-II (EC432)		
T	PE(HS402)	FOC(EC474)/TS	MCS(EC472)/SD		LIBRARY		
F	SICT(EC484)/WVD	MCS(EC472)/SD	WC(EC462)/SJ		LIBRARY		


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MTech Semester II(Communication & Signal Processing)(Room: LH 7)

Day	9:00am -10:00am	10:00am -11:00am	11:00am -12:00am	12:00am 2:00pm	-	2:00pm-3:00pm	3:00pm- 4:00pm	4:00pm -5:00pm
M	IP LAB(EC516)/KPD,Babloo			LUNCH		AME(EC506)/SJ	ADC(504)JK	DC(EC552)/SD
T						ADC(504)JK	AME(EC506)/SJ	IP(EC594)/KPD
W						IP(EC502)/KPD	AME(EC506)/SJ	
T						DC(EC552)/SD	IP(EC594)/KPD	
F						ADC(504)JK	DC(EC552)/SD	

MTech Semester II(VLSI)(Room: LH 8)

Day	9:00am -10:00am	10:00am -11:00am	11:00am -12:00am	12:00am 2:00pm	-	2:00pm-3:00pm	3:00pm- 4:00pm	4:00pm -5:00pm
M	SS LAB(VL514)/CNG,Bikesh			LUNCH		SICT(M)(VL502)/CNG	RTOS(VL506)/RC	MT(VL532)/CNG
T						LPVLSI(VL504)/AR	RTOS(VL506)/RC	MT(VL532)/CNG
W						RTOS(VL506)/RC	SICT(M)(VL502)/CNG	
T						MT(VL532)/CNG	LPVLSI(VL504)/AR	
F						LPVLSI(VL504)/AR	SICT(M)(VL502)/CNG	



**Prof. A Dinamani Singh
(HOD,ECE)**